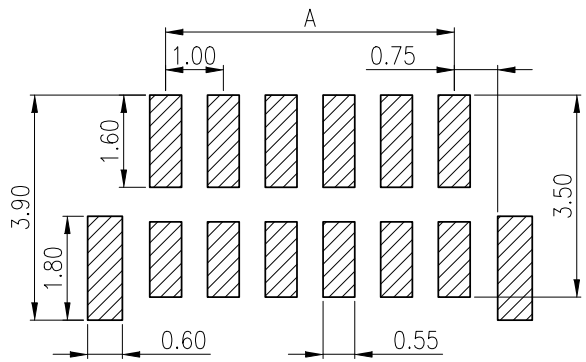
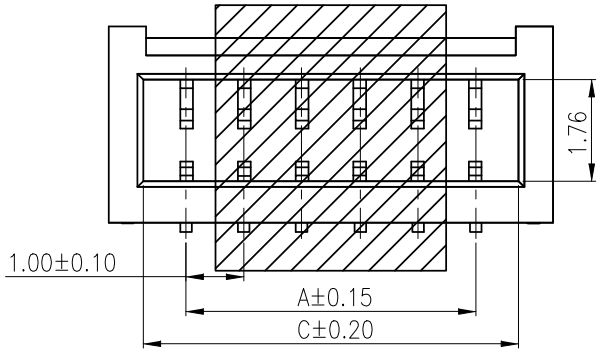
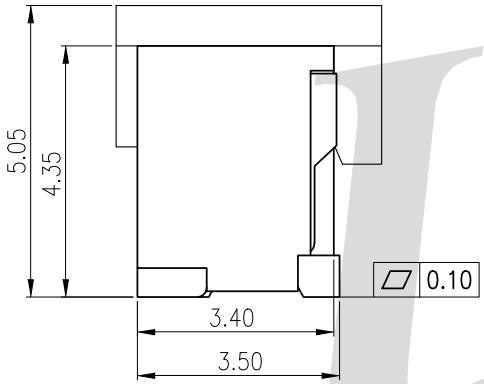
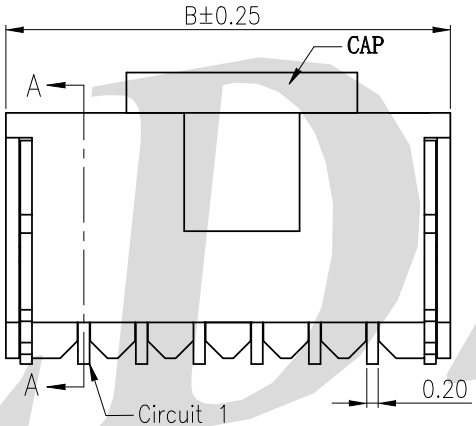
RoHS Lead-free

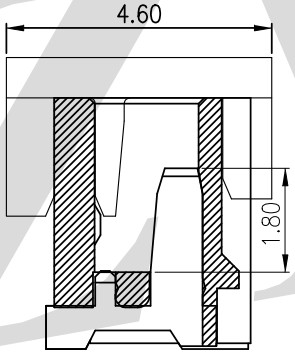




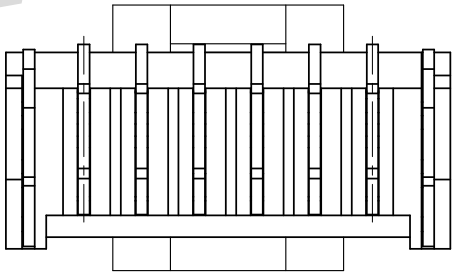
P.C.B Layout







SECTION A–A
SCALE 1:1



SPECIFICATIONS :
Voltage Rating：125V AC / DC
Current Rating： 1A AC / DC
Withstanding Voltage：500V AC/minute
Temperature Range：-25°C~+85°C
Insulation Resistance：100MΩ Min.
Contact Resistance：20mΩ Max.
*Mates with LDZY A1002 series Housing
Material：
Wafer：PA9T UL94V-0,Natural
Conductor：Phorphos Brone/Matte Tin Plating
Solder Tabs：Phorphos Brone/Matte Tin Plating

A1002 W V A S - NP - 9T NA T R
① ②③④⑤ ⑥ ⑦ ⑧ ⑨⑩

① Series No.
② Category：W-wafer
③ Welding Board Angle: V- Straight 180° R-Vertical 90°
④ Colloid Feature：Blank-Common,A-. . .
⑤ Welding Way：Blank-DIP Type; S-SMT Type;
⑥ Circuit No.：02-2PIN,
⑦ Material：9T-PA9T,UL94V-0;
⑧ Color：NA-Natural color
⑨ Plating Method：T-Matte Tin Plating
⑩ Option：R-卷装加盖

Ordering Information & Dimensions

Poles	Dimensions(mm)			Poles	Dimensions(mm)		
	A	B	C		A	B	C
02	1.00	3.70	2.50	11	10.00	12.70	11.50
03	2.00	4.70	3.50	12	11.00	13.70	12.50
04	3.00	5.70	4.50	13	12.00	14.70	13.50
05	4.00	6.70	5.50	14	13.00	15.70	14.50
06	5.00	7.70	6.50	15	14.00	16.70	15.50
07	6.00	8.70	7.50	16	15.00	17.70	16.50
08	7.00	9.70	8.50	17	16.00	18.70	17.50
09	8.00	10.70	9.50	18	17.00	19.70	18.50
10	9.00	11.70	10.50	19	18.00	20.70	19.50
				20	19.00	21.70	20.50

						一般公差 GENERAL TOLERANCE .X ±0.30 .XX ±0.20 .XXX ±0.10	LDZY Electronics 东莞市蓝雁电子有限公司 DongGuan LDZY Electronics CO.,LTD.				料号 PART NO A1002WVAS-NP-9TNATR
..											品名 TITLE 1.00mmSignle Row 180°SMT
							核准 APPD	审核 CHK	绘图 DR	日期 DATE	
日期 DATE	旧版次 OLD REV	新版次 NEW REV	变更内容 DESCRIPTION	审核 CHK	核准 APPD	角法 PROJECT	兰 雄	唐文舜	W L	2025/04/29	单位 UNIT mm / 比例 SCALE 1/1 / 图号 NUMBER LDZY-8158 / 版次 REV A