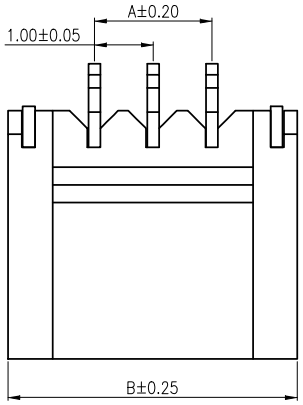
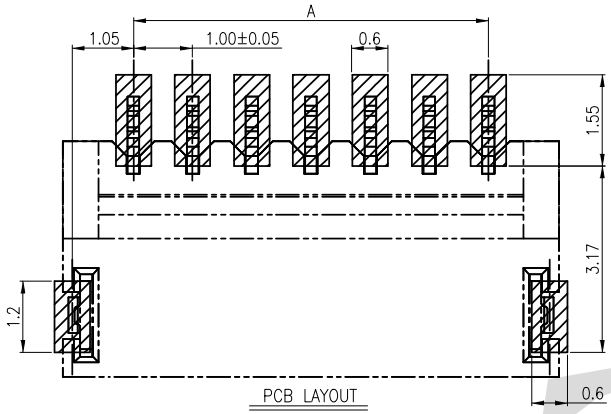
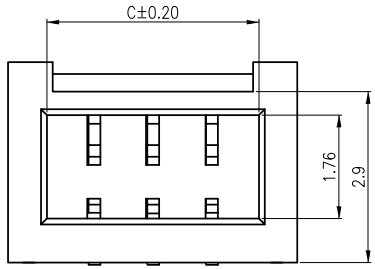
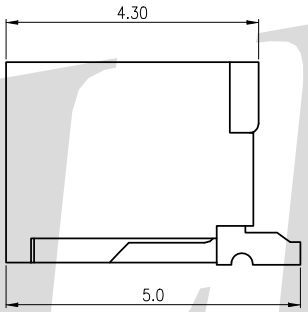
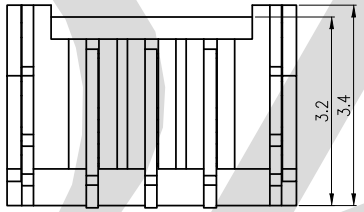
RoHS Lead-free

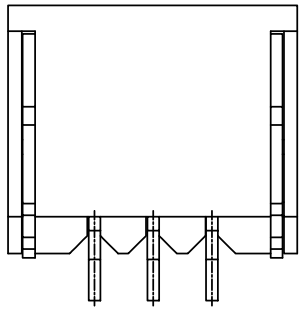


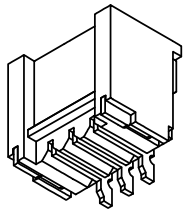


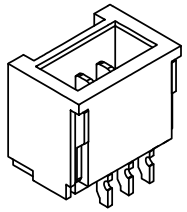












SPECIFICATIONS :
Voltage Rating：125V AC / DC
Current Rating： 1A AC / DC
Withstanding Voltage：500V AC/minute
Temperature Range：-25°C~+85°C
Insulation Resistance：100MΩ Min.
Contact Resistance：20mΩ Max.
*Mates with LDZY A1002 series Housing
Material：
Wafer：PA9T UL94V-0,Natural
Conductor：Phorphos Brone/Matte Tin Plating
Solder Tabs：Phorphos Brone/Matte Tin Plating

A1002 W R A S - NP - 9T NA T - A
① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩

① Series No.
② Category：W-wafer
③ Welding Board Angle: V- Straight 180° R-Vertical 90°
④ Colloid Feature：Blank-Common,A-. . .
⑤ Welding Way：Blank-DIP Type; S-SMT Type; I-IDC Type
⑥ Circuit No.：02-20PIN
⑦ Material：9T-PA9T,UL94V-0;
⑧ Color：NA-Natural
⑨ Plating Method：T-Matte Tin Plating
⑩ Other:A-LDZY-8158

Poles	A	B	C	Poles	A	B	C
2	1.00	3.90	2.50	11	10.00	12.90	11.50
3	2.00	4.90	3.50	12	11.00	13.90	12.50
4	3.00	5.90	4.50	13	12.00	14.90	13.50
5	4.00	6.90	5.50	14	13.00	15.90	14.50
6	5.00	7.90	6.50	15	14.00	16.90	15.50
7	6.00	8.90	7.50	16	15.00	17.90	16.50
8	7.00	9.90	8.50	17	16.00	18.90	17.50
9	8.00	10.90	9.50	18	17.00	19.90	18.50
10	9.00	11.90	10.50	19	18.00	20.90	19.50
				20	19.00	21.90	20.50

						一般公差 GENERAL TOLERANCE .X ±0.30 .XX ±0.20 .XXX ±0.10	LDZY Electronics 东莞市蓝雁电子有限公司 DongGuan LDZY Electronics CO.,LTD.				料号 PART NO A1002WRAS-NP-9TNAT-A
							核准 APPD	审核 CHK	绘图 DR	日期 DATE	品名 TITLE 1.00mmSignle Row 90°SMT
日期 DATE	旧版次 OLD REV	新版次 NEW REV	变更内容 DESCRIPTION	审核 CHK	核准 APPD	角法 PROJECT			2025/05/15	单位 UNIT mm	比例 SCALE 1/1	图号 NUMBER LDZY-8158	版次 REV A