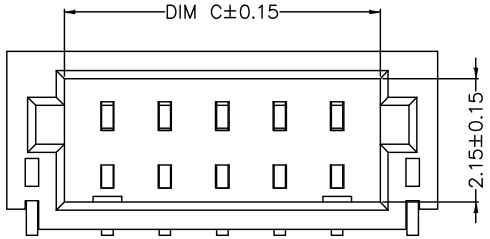
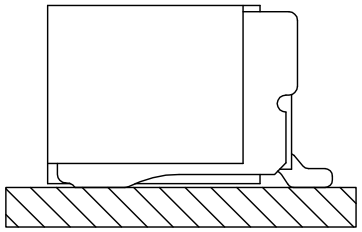
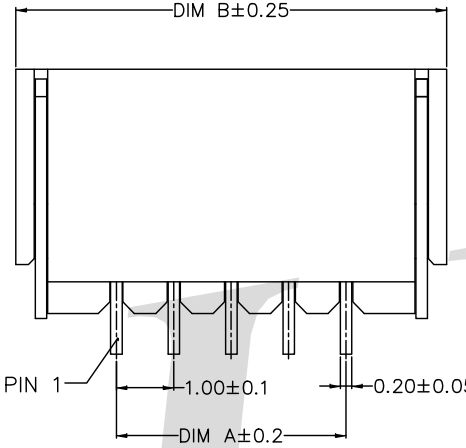
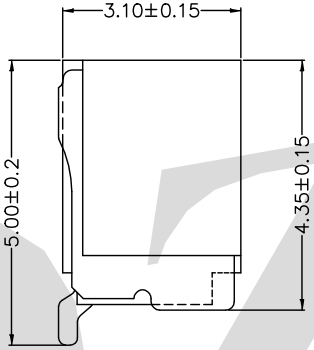
RoHS Lead-free

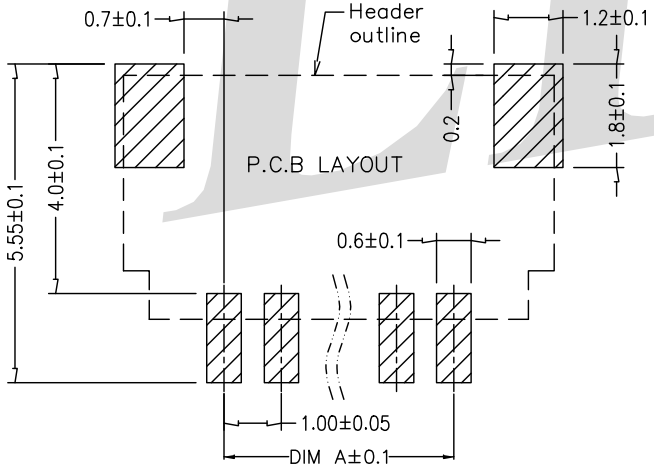




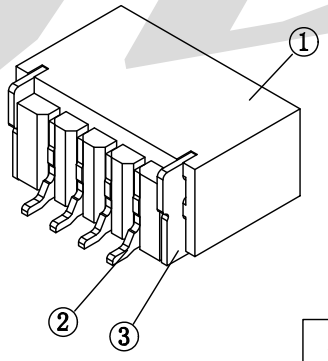
Assembly Layout







PCB寸法
PCB Layout



■ Specifications :

- Voltage rating: 50V AC / DC
- Current rating: 1A AC / DC
- Withstanding voltage: 500V AC/minute
- Temperature range: -25°C~+85°C
- Insulation resistance:1000Ω Min
- Contact resistance: 20mΩ Max

A1001 W R S- NP - 9T BG T - HC
① ②③④⑤ ⑥ ⑦ ⑧ ⑨

① Series No.
② Category:W-wafer
③ Welding Board Angle: R- 90°
④ Soldering Type：S-SMT Type,
⑤ Circuit No.：02-2PIN, 03-3PIN ...
⑥ Material：9T-PA9T,UL94V-0
⑦ Color：BG-Beige
⑧ Plating Method： T-Tin-plated
⑨ Other：HC - HC Type LDZY-81513

Ordering Information & Dimensions

Circuits	Dimensions			Circuits	Dimensions		
	DIM A	DIM B	DIM C		DIM A	DIM B	DIM C
02	1.00	4.50	2.60	12	11.00	14.50	12.60
03	2.00	5.50	3.60	13	12.00	15.50	13.60
04	3.00	6.50	4.60	14	13.00	16.50	14.60
05	4.00	7.50	5.60	15	14.00	17.50	15.60
06	5.00	8.50	6.60	16	15.00	18.50	16.60
07	6.00	9.50	7.60	17	16.00	19.50	17.60
08	7.00	10.50	8.60	18	17.00	20.50	18.60
09	8.00	11.50	9.60	19	18.00	21.50	19.60
10	9.00	12.50	10.60	20	19.00	22.50	20.60
11	10.00	13.50	11.60				

序号	名 称	材 料	数 量	附 注
3	焊片 Fitting Nail	磷铜 Phosphor Braonze	2	电镀锡 Tin-Plated
2	端子 Contact	磷铜 Phosphor Braonze	n	电镀锡 Tin-Plated
1	基座 Wafer	PA9T(UL94V-0)	1	本色

日期 DATE	旧版次 OLD REV	新版次 NEW REV	变更内容 DESCRIPTION	审核 CHK	核准 APPD

一般公差 GENERAL TOLERANCE
.X ±0.3
.XX ±0.2
.XXX ±0.1

角法 PROJECT

LDZY 东莞市蓝雁电子有限公司
DongGuan LDZY Electronics CO.,LTD.

核准 APPD	审核 CHK	绘图 DR	日期 DATE
兰 雄	唐文舜	W L	2026/06/30

料号 PART NO: A1001WRS-NP-9TBGT-HC

品名 TITLE: 1.0mm Pitch 180° SMT Single Row Wafer

单位 UNIT	比例 SCALE	图号 NUMBER	版次 REV
mm	1/1	LDZY-81513	A